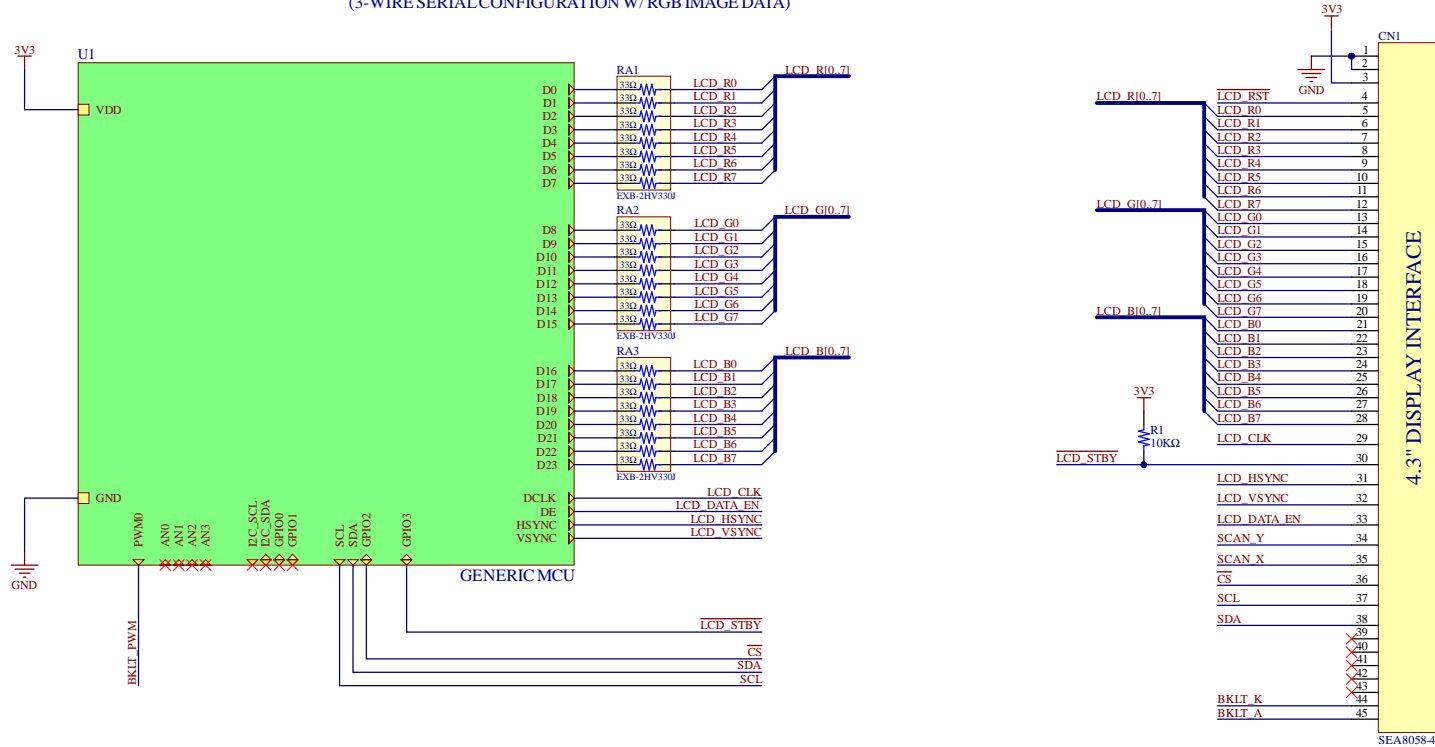
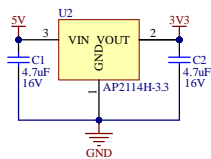


REVISION HISTORY			
REV.	DESCRIPTION	INITIALS	DATE
A1	INITIAL RELEASE	PRW	20230804

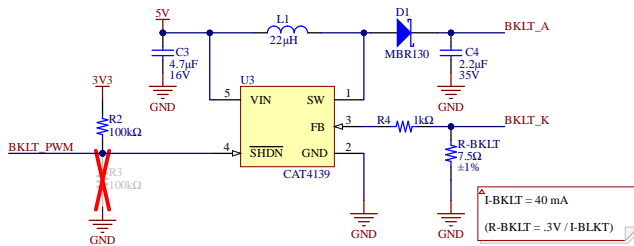
DISPLAYTECH REFERENCE DESIGN
DT043CTFT-IPS
 (3-WIRE SERIAL CONFIGURATION W/ RGB IMAGE DATA)



DISPLAY LOGIC POWER

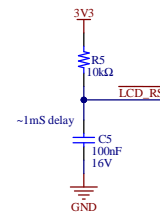


LED BACKLIGHT DRIVER

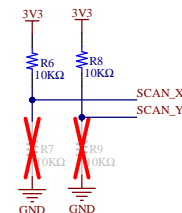


BACKLIGHT WILL TURN ON AT BOOT-UP DUE TO R2 PULL-UP RESISTOR. FOR BACKLIGHT OFF AT BOOT-UP: DEPOPULATE R2, POPULATE R3

RESET TIMING



DISPLAY SCAN CONFIG



STANDARD DISPLAY SCAN DIRECTION (LEFT-TO-RIGHT, TOP-TO-BOTTOM)
 *SEE TABLE FOR ADDITIONAL OPTIONS

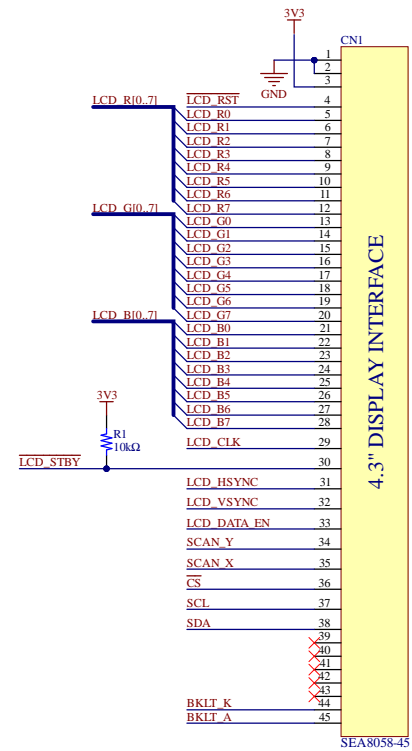
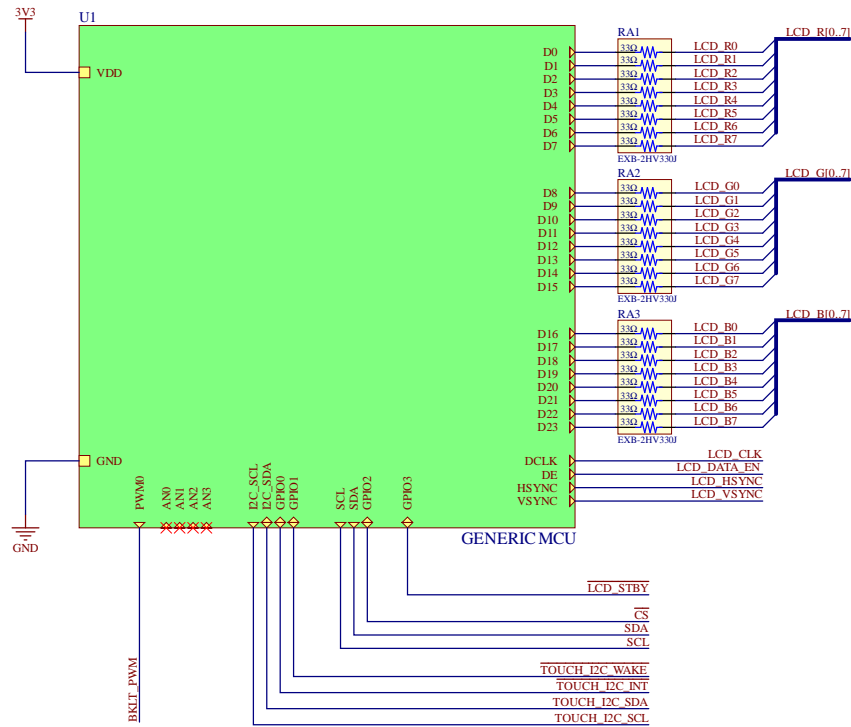
SCAN DIRECTION SETTINGS					
X-SCAN	Y-SCAN	R6	R7	R8	R9
←	↑	/	X	/	X
→	↑	X	/	/	X
←	↓	/	X	/	/
→	↓	X	/	/	X

* X = POPULATE

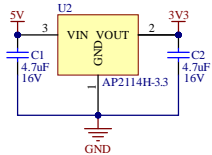
Project: DT043CTFT-IPS		Revision: A1		 a seacompany company	
Description: 4.3 INCH SPI RGB LCD		Sheet Size: A3		Date: 8/10/2023	
Document: Schematic, Reference Design		Sheet: 1 of 3		Time: 5:19:39 PM	
Drawn: PRW		Approved:		DISPLAYTECH 1525 Faraday Ave., Ste. 200 Carlsbad, CA 92008	
File: G:\Shared drives\US Engineering\2 Displaytech\1 - LCD Modules\IPS & TFT\4.3 inch\DT043CTFT-IPS\3					

REVISION HISTORY			
REV	DESCRIPTION	INITIALS	DATE
A1	INITIAL RELEASE	PRW	20230804

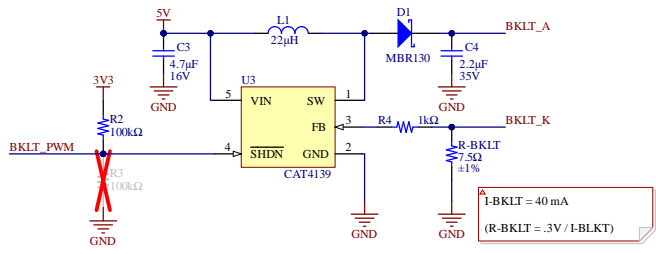
DISPLAYTECH REFERENCE DESIGN
DT043CTFT-IPS-PTS
 (3-WIRE SERIAL CONFIGURATION W/ RGB IMAGE DATA)



DISPLAY LOGIC POWER

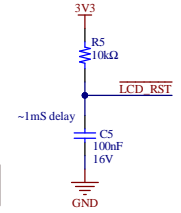


LED BACKLIGHT DRIVER

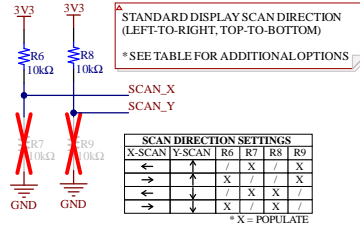


A BKLT WILL TURN ON AT BOOT-UP DUE TO R2 PULL-UP RESISTOR. FOR BACKLIGHT OFF AT BOOT-UP: DEPOPULATE R2, POPULATE R3

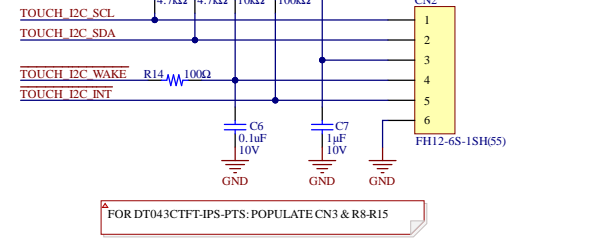
RESET TIMING



DISPLAY SCAN CONFIG



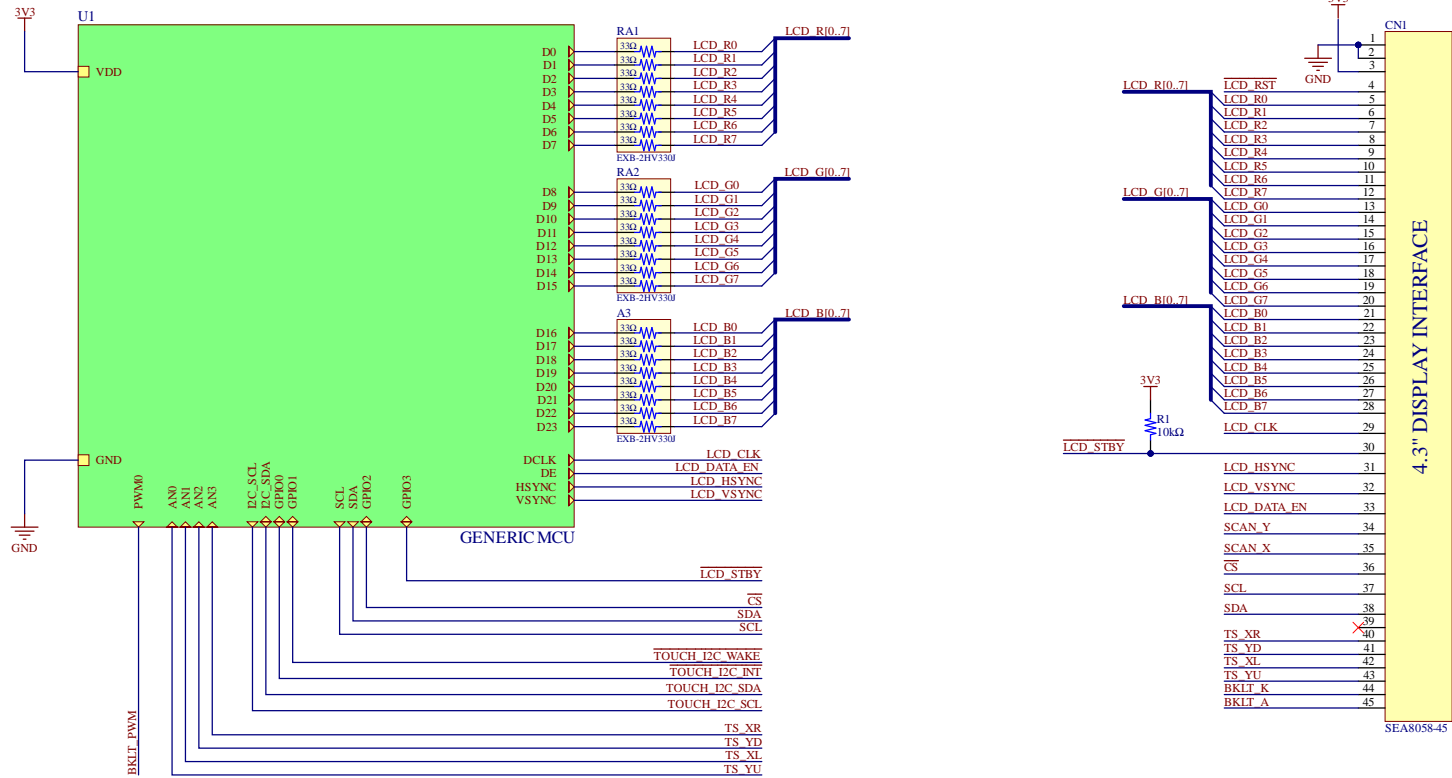
CAPACITIVE TOUCH INTERFACE



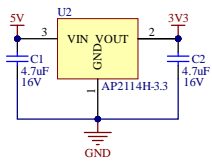
Project: DT043CTFT-IPS-PTS		Revision: A1	
Description: 4.3 INCH SPI RGB LCD W/CAP. TOUCH		Sheet Size: A3	
Document: Schematic, Reference Design	Approved: PRW	Sheet: 2 of 3	Date: 8/10/2023
File: G:\Shared drives\US Engineering\2 Displaytech\1 - LCD Modules\IPS & TFT\4.3 inch\DT043CTFT-IPS\3		Time: 5:21:32 PM	
		 a seacom company	
		DISPLAYTECH 1525 Faraday Ave., Ste. 200 Carlsbad, CA 92008	

REVISION HISTORY			
REV.	DESCRIPTION	INITIALS	DATE
A1	INITIAL RELEASE	PRW	20230804

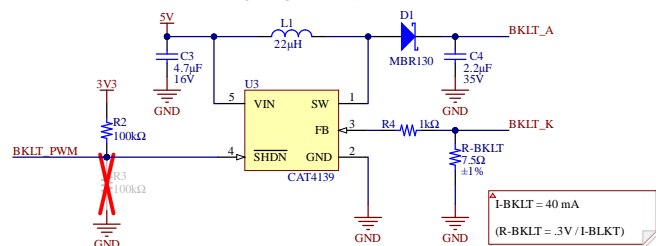
DISPLAYTECH REFERENCE DESIGN
DT043CTFT-IPS-TS
 (3-WIRE SERIAL CONFIGURATION W/ RGB IMAGE DATA)



DISPLAY LOGIC POWER



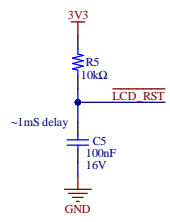
LED BACKLIGHT DRIVER



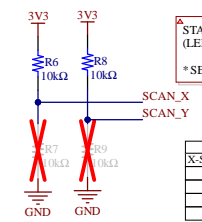
BACKLIGHT WILL TURN ON AT BOOT-UP DUE TO R2 PULL-UP RESISTOR. FOR BACKLIGHT OFF AT BOOT-UP, DEPOPULATE R2, POPULATE R3

I-BKLT = 40 mA
 (R-BKLT = .3V / I-BKLT)

RESET TIMING



DISPLAY SCAN CONFIG



STANDARD DISPLAY SCAN DIRECTION (LEFT-TO-RIGHT, TOP-TO-BOTTOM)
 *SEE TABLE FOR ADDITIONAL OPTIONS

SCAN DIRECTION SETTINGS					
X-SCAN	Y-SCAN	R6	R7	R8	R9
←	↑	X	/	X	X
←	↓	X	/	X	X
→	↑	X	/	X	/
→	↓	X	/	X	/

*X = POPULATE

Project: DT043CTFT-IPS-TS		a seacom company	
Description: 4.3 INCH SPI RGB LCD W/RES. TOUCH		Revision: A1	
Document: Schematic, Reference Design	Sheet Size: A3	Date: 8/10/2023	
Drawn: PRW	Approved:	Sheet: 3 of 3	Time: 5:20:32 PM
File: G:\Shared drives\US Engineering\2 Displaytech\1 - LCD Modules\IPS & TFT\4.3 inch\DT043CTFT-IPS\3			

DISPLAYTECH
 1525 Faraday Ave., Ste. 200
 Carlsbad, CA 92008