```
   MainLCD_Command(0x00E5); MainLCD_Data(0x8000);// Set the Vcore voltage and this setting is must.

   MainLCD_Command(0x0000); MainLCD_Data(0x0001);// Start internal OSC.

   MainLCD_Command(0x0001); MainLCD_Data(0x0100);// set SS and SM bit

   MainLCD_Command(0x0002); MainLCD_Data(0x0700);// set 1 line inversion

   MainLCD_Command(0x0003); MainLCD_Data(0x1030);// set GRAM write direction and BGR=1.

   MainLCD_Command(0x0004); MainLCD_Data(0x0000);// Resize register

   MainLCD_Command(0x0008); MainLCD_Data(0x0808);// set the back porch and front porch

   MainLCD_Command(0x0009); MainLCD_Data(0x0000);// set non-display area refresh cycle ISC[3:0]

   MainLCD_Command(0x000A); MainLCD_Data(0x0000);// FMARK function

   MainLCD_Command(0x000C); MainLCD_Data(0x0000);// RGB interface setting

   MainLCD_Command(0x000D); MainLCD_Data(0x0000);// Frame marker Position

   MainLCD_Command(0x000F); MainLCD_Data(0x0000);// RGB interface polarity

   MainLCD_Command(0x0010); MainLCD_Data(0x0000);// Power On sequence  //SAP, BT[3:0], AP, DSTB, SLP,
STB
   Delayms(15);
   MainLCD_Command(0x0011); MainLCD_Data(0x0007);// DC1[2:0], DC0[2:0], VC[2:0]
   MainLCD_Command(0x0012); MainLCD_Data(0x0000);// VREG1OUT voltage
   MainLCD_Command(0x0013); MainLCD_Data(0x0000);// VDV[4:0] for VCOM amplitude
   MainLCD_Command(0x0010); MainLCD_Data(0x16B0);// SAP, BT[3:0], AP, DSTB, SLP, STB
   Delayms(15);
   MainLCD_Command(0x0011); MainLCD_Data(0x0007);// R11h=0x0001 at VCI=3.3V DC1[2:0], DC0[2:
   Delayms(15);
   MainLCD_Command(0x0012); MainLCD_Data(0x011A);// R11h=0x0138 at VCI=3.3V VREG1OUT voltage
   Delayms(15);
   MainLCD_Command(0x0013); MainLCD_Data(0x1B00);// R11h=0x1800 at VCI=2.8V VDV[4:0] for VCO
   MainLCD_Command(0x0029); MainLCD_Data(0x0012);// setting VCM for VCOMH  0018-0012
   MainLCD_Command(0x0020); MainLCD_Data(0x0000);// GRAM horizontal Address
   MainLCD_Command(0x0021); MainLCD_Data(0x0000);// GRAM Vertical Address
   MainLCD_Command(0x0030); MainLCD_Data(0x0000);// - Adjust the Gamma Curve -//
   MainLCD_Command(0x0031); MainLCD_Data(0x0707);
   MainLCD_Command(0x0032); MainLCD_Data(0x0707);
   MainLCD_Command(0x0035); MainLCD_Data(0x0000);
   MainLCD_Command(0x0036); MainLCD_Data(0x001F);
   MainLCD_Command(0x0037); MainLCD_Data(0x0105);
   MainLCD_Command(0x0038); MainLCD_Data(0x0002);
   MainLCD_Command(0x0039); MainLCD_Data(0x0707);
   MainLCD_Command(0x003C); MainLCD_Data(0x0602);
   MainLCD_Command(0x003D); MainLCD_Data(0x1000);// - Adjust the Gamma Curve -//
   Delayms(15);
   MainLCD_Command(0x0050); MainLCD_Data(0x0000);// Horizontal GRAM Start Address
   MainLCD_Command(0x0051); MainLCD_Data(0x00EF);// Horizontal GRAM End Address
   MainLCD_Command(0x0052); MainLCD_Data(0x0000);// Vertical GRAM Start Address
   MainLCD_Command(0x0053); MainLCD_Data(0x013F);// Vertical GRAM Start Address
   MainLCD_Command(0x0060); MainLCD_Data(0x2700);// Gate Scan Line
   MainLCD_Command(0x0061); MainLCD_Data(0x0001);// NDL,VLE, REV
   MainLCD_Command(0x006A); MainLCD_Data(0x0000);// set scrolling line
   MainLCD_Command(0x0080); MainLCD_Data(0x0000);//- Partial Display Control -//
   MainLCD_Command(0x0081); MainLCD_Data(0x0000);
   MainLCD_Command(0x0082); MainLCD_Data(0x0000);
   MainLCD_Command(0x0083); MainLCD_Data(0x0000);
   MainLCD_Command(0x0084); MainLCD_Data(0x0000);
   MainLCD_Command(0x0085); MainLCD_Data(0x0000);
   MainLCD_Command(0x0090); MainLCD_Data(0x0012);//- Panel Control -//
   MainLCD_Command(0x0092); MainLCD_Data(0x0000);
   MainLCD_Command(0x0093); MainLCD_Data(0x0003);
   MainLCD_Command(0x0095); MainLCD_Data(0x0110);
   MainLCD_Command(0x0097); MainLCD_Data(0x0000);
   MainLCD_Command(0x0098); MainLCD_Data(0x0000);//- Panel Control -//
   MainLCD_Command(0x0007); MainLCD_Data(0x0173);//Display Control and display ON
   Delayms(15);
   MainLCD_Command(0x0022);                    //Write Data to GRAM
```