

## dt042tft series sample program

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void MainLCD_Initial()
{
//H2 4.2'' // ILI9326
//***** Start Initial Sequence *****//
WMLCDCOM(0x0702);WMLCDDATA(0x3008);// Set internal timing, don't change this value
WMLCDCOM(0x0705);WMLCDDATA(0x0036);// Set internal timing, don't change this value
WMLCDCOM(0x070B);WMLCDDATA(0x1213);// Set internal timing, don't change this value
WMLCDCOM(0x0001);WMLCDDATA(0x0100);// set SS and SM bit
WMLCDCOM(0x0002);WMLCDDATA(0x0100);// set 1 line inversion
WMLCDCOM(0x0003);WMLCDDATA(0x1098);// set GRAM write direction and BGR=1, Landscape 10A8, Portrait
1030
WMLCDCOM(0x0008);WMLCDDATA(0x0808);// set the back porch and front porch
WMLCDCOM(0x0009);WMLCDDATA(0x0000);// set non-display area refresh cycle ISC[3:0]
WMLCDCOM(0x000C);WMLCDDATA(0x0000);// RGB interface setting
WMLCDCOM(0x000F);WMLCDDATA(0x0000);// RGB interface polarity
//*****Power On sequence *****//
WMLCDCOM(0x0100);WMLCDDATA(0x0000);// SAP,BT[3:0],AP,DSTB,SLP,STB
WMLCDCOM(0x0102);WMLCDDATA(0x0000);// VREG1OUT voltage
WMLCDCOM(0x0103);WMLCDDATA(0x0000);// VDV[4:0] for VCOM amplitude
Delays(200); // Dis-charge capacitor power voltage
WMLCDCOM(0x0100);WMLCDDATA(0x1190);// SAP,BT[3:0],AP,DSTB,SLP,STB
WMLCDCOM(0x0101);WMLCDDATA(0x0017);// DC1[2:0],DC0[2:0],VC[2:0]
Delays(50); // Delay 50ms
WMLCDCOM(0x0102);WMLCDDATA(0x01BD);// VREG1OUT voltage
Delays(50); // Delay 50ms
WMLCDCOM(0x0103);WMLCDDATA(0x3500);// VDV[4:0] for VCOM amplitude
WMLCDCOM(0x0281);WMLCDDATA(0x0017);// VCM[5:0] for VCOMH
Delays(50);
WMLCDCOM(0x0200);WMLCDDATA(0x0000);// GRAM horizontal Address
WMLCDCOM(0x0201);WMLCDDATA(0x0000);// GRAM Vertical Address
// ----- Adjust the Gamma Curve -----//
WMLCDCOM(0x0300);WMLCDDATA(0x0000);
WMLCDCOM(0x0301);WMLCDDATA(0x0707);
WMLCDCOM(0x0302);WMLCDDATA(0x0606);
WMLCDCOM(0x0305);WMLCDDATA(0x0006);
WMLCDCOM(0x0306);WMLCDDATA(0x0A09);
WMLCDCOM(0x0307);WMLCDDATA(0x0203);
WMLCDCOM(0x0308);WMLCDDATA(0x0005);
WMLCDCOM(0x0309);WMLCDDATA(0x0007);
WMLCDCOM(0x030C);WMLCDDATA(0x0400);
WMLCDCOM(0x030D);WMLCDDATA(0x000A);
//----- Set GRAM area -----//
WMLCDCOM(0x0210);WMLCDDATA(0x0000);// Horizontal GRAM Start Address
WMLCDCOM(0x0211);WMLCDDATA(0x00EF);// Horizontal GRAM End Address
WMLCDCOM(0x0212);WMLCDDATA(0x0000);// Vertical GRAM Start Address
WMLCDCOM(0x0213);WMLCDDATA(0x01AF);// Vertical GRAM Start Address
WMLCDCOM(0x0400);WMLCDDATA(0x3500);// Gate Scan Line 432 lines
WMLCDCOM(0x0401);WMLCDDATA(0x0001);// NDL,VLE,REV
WMLCDCOM(0x0404);WMLCDDATA(0x0000);// set scrolling line
//----- Partial Display Control -----//
WMLCDCOM(0x0500);WMLCDDATA(0x0000);// Partial Image 1 Display Position
WMLCDCOM(0x0501);WMLCDDATA(0x0000);// Partial Image 1 RAM Start/End Address
WMLCDCOM(0x0502);WMLCDDATA(0x0000);// Partial Image 1 RAM Start/End Address
WMLCDCOM(0x0503);WMLCDDATA(0x0000);// Partial Image 2 Display Position
WMLCDCOM(0x0504);WMLCDDATA(0x0000);// Partial Image 2 RAM Start/End Address
WMLCDCOM(0x0505);WMLCDDATA(0x0000);// Partial Image 2 RAM Start/End Address
//----- Panel Control -----//
WMLCDCOM(0x0010);WMLCDDATA(0x0010);// DIVI[1:0];RTNI[4:0]
WMLCDCOM(0x0011);WMLCDDATA(0x0600);// NOWI[2:0];SDTI[2:0]
WMLCDCOM(0x0020);WMLCDDATA(0x0002);// DIVE[1:0];RTNE[5:0]
WMLCDCOM(0x0007);WMLCDDATA(0x0173);// 262K color and display ON
Delays(20);
WMLCDCOM(0x0202); // Write Data to GRAM
}

```